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Serial No. 09/938,106

**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Corisis et al.

Serial No.: 09/942,183

Filed: August 29, 2001

For: ROUTING ELEMENT FOR USE IN MULTI-CHIP MODULES, MULTI-CHIP MODULES INCLUDING THE ROUTING ELEMENT AND METHODS

Examiner: Unknown

Group Art Unit: 2811

**Attorney Docket No.:** 4813US (01-0104)

## CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C F.R. § 18(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D C 20231

December 3, 2001

Signature of registered practitioner or other person having reasonable basis to expect parling to occur on date of deposit shown pursuant to 37 C.F.R. § 1.8(a)(1)(ii)

Deidra Pfeil
Typed/printed name of person whose signature is contained above

## RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS

Commissioner for Patents Washington, D.C. 20231

Sir:

Enclosed is a copy of the Notice to File Corrected Application Papers, along with the Formal Drawings required in connection with the above-referenced application. These drawings are being filed within two (2) months from the mailing date of the Notice to File Corrected Application Papers.

Respectfully submitted,

Brick G. Power

Registration No. 38,581

Attorney for Applicant

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Date: December 3, 2001

Enclosures: Copy of Notice To File Corrected Application Papers

Letter to Chief Draftsman

Transmittal of Formal Drawings Formal Drawings (3 sheets)